

<b>Notice of References Cited</b>	Application/Control No. 10/688,763	Applicant(s)/Patent Under Reexamination YIP ET AL.	
	Examiner Ben C. Wang	Art Unit 2196	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,175,364 b1	01-2001	Wong et al.	715/763
*	B	US-7,058,928 b2	06-2006	Wygodny et al.	717/128
	C	US-			
	D	US-			
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	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Austin et al., SimpleScalar: An Infrastructure for Computer System Modeling, Feb. 2002, IEEE
	V	Drongowski et al., Profiling to Tune C Programs on Alpha, April 6, 2001, Hewlett Packard Company
	W	Hollingsworth et al., The Clipper Processor: Instruction Set Architecture And Implementation, February 1989, ACM
	X	Sherwood et al., A Pipelined Memory Architecture for High Throughput Network Processors, June 2003, IEEE

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.